

**ABSTRACT OF THE DISCLOSURE**

Disclosed is an LCD system including an LCD panel having a plurality of data lines, a plurality of gate lines intersecting the data lines in a substantially perpendicular state, and a plurality of pixel electrodes arranged in a matrix configuration and each having a switch connected to one of the gate lines and one of the data lines; a gate driver for successively applying a gate voltage to the gate lines to turn on the switches; a data driver for applying a gray voltage, corresponding to image data signals, to the data lines; and a printed circuit board having a timing controller for sending both the image data signals and a shift clock signal to the data driver, a first signal wire through which the shift clock signal is transmitted, and a second signal wire through which a first clock signal having a frequency equal to and a phase difference of  $90^\circ$  to  $270^\circ$  compared to the shift clock signal. In another aspect, the printed circuit board has a timing controller for generating first and second image data signals and generating first and second shift clock signals having a phase difference of  $90^\circ$  to  $270^\circ$  that respectively shift the first and second image data signals, first and second image data signal wires through which the first and second image data signals are respectively transmitted, and first and second shift clock signal wires through which the first and second shift clock signals are respectively transmitted.